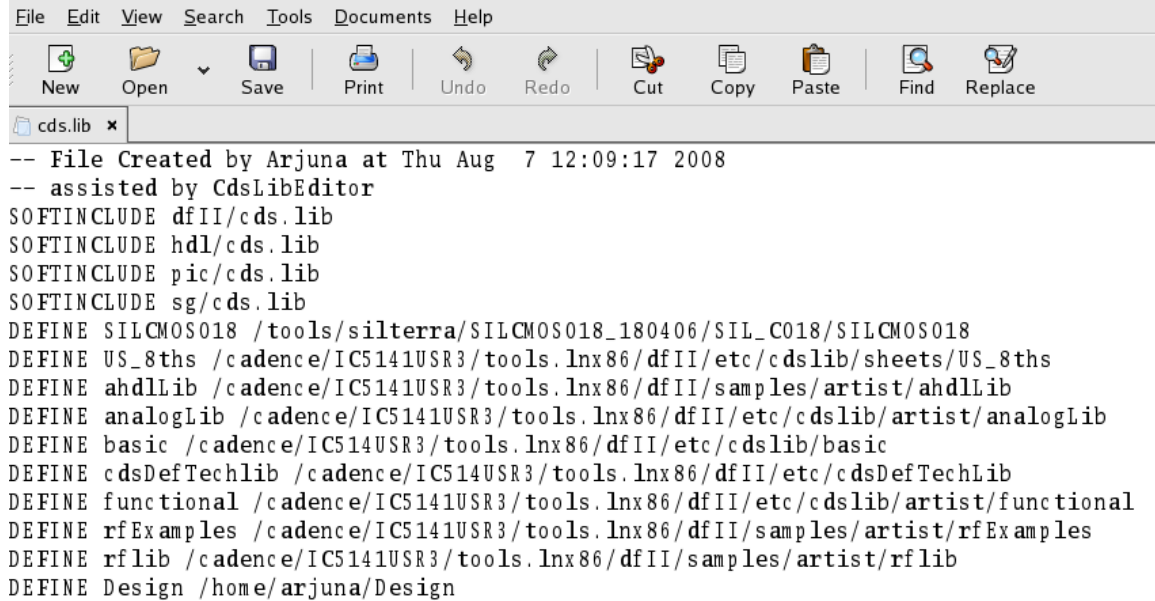


# Basic Cadence Simulation Setup for Silterra

By A. Marzuki

August 2008

## Cds.lib



```
-- File Created by Arjuna at Thu Aug 7 12:09:17 2008
-- assisted by CdsLibEditor
SOFTINCLUDE dfII/cds.lib
SOFTINCLUDE hdl/cds.lib
SOFTINCLUDE pic/cds.lib
SOFTINCLUDE sg/cds.lib
DEFINE SILCMOS018 /tools/silterra/SILCMOS018_180406/SIL_C018/SILCMOS018
DEFINE US_8ths /cadence/IC5141USR3/tools.lnx86/dfII/etc/cdslib/sheets/US_8ths
DEFINE ahdlLib /cadence/IC5141USR3/tools.lnx86/dfII/samples/artist/ahdlLib
DEFINE analogLib /cadence/IC5141USR3/tools.lnx86/dfII/etc/cdslib/artist/analogLib
DEFINE basic /cadence/IC5141USR3/tools.lnx86/dfII/etc/cdslib/basic
DEFINE cdsDefTechlib /cadence/IC5141USR3/tools.lnx86/dfII/etc/cdsDefTechLib
DEFINE functional /cadence/IC5141USR3/tools.lnx86/dfII/etc/cdslib/artist/functional
DEFINE rfExamples /cadence/IC5141USR3/tools.lnx86/dfII/samples/artist/rfExamples
DEFINE rflib /cadence/IC5141USR3/tools.lnx86/dfII/samples/artist/rflib
DEFINE Design /home/arjuna/Design
```

## Model library setup of silterra model

